



ISO 18000-63 / EPC™ Gen2 RFID IC

General Description

EM4124 is a certified EPC™ Gen2 IC and compliant with ISO/IEC 18000-63 (formerly known as 18000-6 Type C).

Each chip is manufactured with a 64-bit Unique Identifier to ensure full traceability. The EM4124 has 176 bits of non-volatile memory (16 bit PC Word, 96 bit EPC Code, 32 bit Kill Password, and 32 bit Access Password), enabling the support of ISO or EPC data structures. Each chip is delivered with a 96-bit EPC encoding with pre-encoded support for Multi-vendor Chip-based Serialization (MCS).

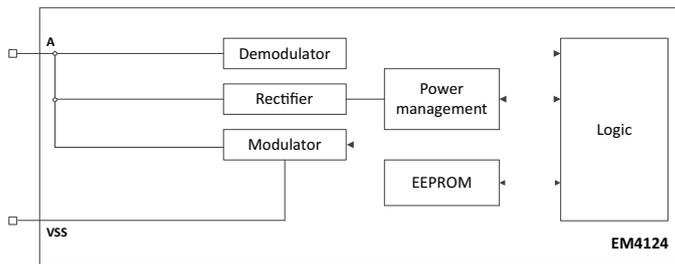
EM4124 achieves a typical read sensitivity of -19 dBm at the chip level, which translates into a typical -21 dBm sensitivity at the tag level for a dipole-like antenna with 2 dBi gain (25us TARI, 250 kHz BLF).

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Features

- | ISO 18000-63 compliant
- | EPC Class-1 Generation-2 certified
- | Supports Multi-vendor Chip-based Serialization
- | 96-bit EPC / UUI encodings are supported
- | 64-bit manufacturer-programmed & locked Unique Identifier (TID / UID)
- | -19 dBm (-21 dBm) typical IC (tag) read sensitivity
- | 32-bit password-protected Kill command
- | 32-bit password-protected Access command
- | Optional deactivate function to address privacy concerns for retail applications
- | Extended temperature range (-40 °C to +85 °C)

IC Block Diagram



Applications

- | Supply chain management
- | Tracking and tracing
- | Container identification
- | Access control
- | Asset control

Ordering Information

The versions below are considered standards and should be readily available. For other versions or other delivery form, please contact EM Microelectronic-Marin S.A. For samples, please, order exclusively from the standard version.

Part Number	Protocol	Package / Die Form	Delivery Form	Comment
EM4124WS6U	EPC Gen2/ ISO 18000-63	Sawn wafer / bumped die - 6 mils thickness	Wafer sawn on frame	Certified version
EM4124WS6U-201	EPC Gen2/ ISO 18000-63	Sawn wafer / bumped die - 6 mils thickness	Wafer sawn on frame	Mute version
EM4124VXYYY-%%%	Custom	Custom	Custom	

Typical Configuration

